

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Toshinari Takayanagi

Title:

NEGATIVE BIAS TEMPERATURE INSTABILITY (NBTI)

PRECONDITIONING OF MATCHED DEVICES

Application No.: 10/664,104

Filed:

September 17, 2003

Examiner:

Not Yet Assigned

Group Art Unit: 2811

Atty. Docket No.: 004-9196

July 9, 2004

Mail Stop Amendment **COMMISSIONER FOR PATENTS** P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT 37 C.F.R. § 1.97(b)

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the undersigned brings the patents, publications, applications or other information identified in the attached:

- \boxtimes Form(s) PTO-1449 (1 pages), including copy(ies) of 1 reference(s).
- Other: List of Pending U.S. Patent Applications, including copies of 3 applications.

to the Examiner's attention in the above-identified application. Citation of such information shall not be construed as:

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- a representation that a search has been made, other than as described below; or 2.
- 3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

Pursuant to 1276 OG 55 (August 5, 2003), Information Disclosure Statements may be filed without copies of U.S. Patents and Published Applications in Patent Applications filed after June 30, 2003.

For each item of information listed that is not in the English language, the undersigned has provided a concise explanation of the relevance through (i) an English language abstract, (ii) an English language equivalent application, or (iii) if cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action that indicates the degree of relevance found by the foreign office.

FEE AUTHORIZATION

This Information Disclosure Statement is filed within three months of the filing
date of a national application other than a continued prosecution application under
§ 1.53(d) or within three months of entry of the national stage as set forth in
§ 1.491 in an international application. Therefore, no fee is required.

The undersigned believes that this Information Disclosure Statement is being filed before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.

If however, this Information Disclosure Statement is filed after the period specified in § 1.97(b), the undersigned hereby authorizes the Commissioner to charge the fee set forth in § 1.17(p) to Deposit Account No. 50-0631.

CERTIFICATE OF MAILING OR TRANSMISSION
I hereby certify that, on the date shown below, this correspondence is being
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facsimile transmitted to the US Patent and Trademark Office.
Andrew C. Graham Date

Respectfully submitted,

Andrew C. Graham, Reg. No. 36,531

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EXPRESS MAIL LABEL:

J.S. Departme	nt of Con	nmerce, Patent and Trade	Attorney Docket No.:	004-9196	_			
			Application No.:	10/664,104				
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(Use several sheets if necessary)					Filing Date:	September 17, 2	2003	
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	S				Date Submitted:	July 9, 2004		
A TADEN	ENCA		U.S. Patent Docume	ents				
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		Document	Date	<u> </u>	Country	Yes	No.	
	AO	FR 2 792 760	Oct. 27, 2000	France			X	
	AP	<u> </u>						
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Attorney Docket No.: 004-9196 Application No.: 10/664,104 Applicant(s): Toshinari Takayanagi Filing Date: September 17, 2003 Group Art Unit: 2811 Date Submitted: July 9, 2004

LIST OF PENDING U.S. PATENT APPLICATIONS

The following pending U.S. patent applications are listed for consideration by the Examiner.

- 1) U. S. Patent Application No. 10/664,121, filed September 17, 2003, entitled "Negative Bias Temperature Instability (NBTI) Hardened Sense Amplifier Circuit and Method Therefor," naming inventor, Toshinari Takayanagi.
- 2) U. S. Patent Application No. 10/683,636, filed October 10, 2003, entitled "Test Circuit for Measuring Sense Amplifier and Memory Mismatches," naming inventors, Nadeem N. Eleyan, Howard L. Levy and Jeffrey Y. Su.
- 3) U. S. Patent Application No. 10/683,633, filed October 10, 2003, entitled "Measuring and Correcting Sense Amplifier and Memory Mismatches Using NBTI," naming inventors, Nadeem N. Eleyan, Howard L. Levy and Jeffrey Y. Su.

Applicant(s) are providing a separate list of pending applications, pursuant to MPEP § 609III(D), to avoid publication of the application numbers on any patent that issues from the above-identified application. Applicant(s) request that the Examiner indicate consideration of the pending applications listed above in accordance with MPEP 609III(C)(2) and return a copy of this page with the next communication.